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U.S. UTILITY Patent Application

APPL NUM 10080681	FILING DATE 02/25/2002	CLASS 711 305	SUBCLASS 103	CAU 2824 2787	EXAMINER M. Kim T. HO
**APPLICANTS: Yada Naoki; Ishikawa Eiichi;					
**CONTINUING DATA VERIFIED: No					
** FOREIGN APPLICATIONS VERIFIED: TH JAPAN 2001-097808 03/30/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO XA-9627	
Verified and Acknowledged Examiners's initials					
TITLE : Microcomputer, programming method and erasing method					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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